

A Novel Resume for Mixed Signal Design Engineer Skyler Weaver

Skyler Weaver, *IEEE Student Member*
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Abstract—A brilliant design engineer is presented. He received his HBS in EE from OSU, June 2006, and is currently a Ph.D candidate at OSU under Dr. Moon. His research is currently in mixed signal integrated circuits.

I. INTRODUCTION

As CMOS designs are scaled to smaller technology nodes, many benefits arise as well as challenges; nonetheless, talented circuit designers are required to invent innovative integrated circuit solutions. The circuit designer presented in this resume is a good candidate for solving these new, emerging circuit challenges.

II. QUALIFICATIONS

A. Education

In June 2006, Skyler received his Honors Bachelor of Science (HBS) in Electrical Engineering from Oregon State University (OSU) located in Corvallis, Oregon. He was ranked first in his graduating class with a grade point average (GPA) of 3.93/4.0.

Currently he is Doctor of Philosophy (Ph.D) candidate at OSU studying under Professor Un-Ku Moon. Skyler's research in Electrical Engineering is specifically in the field of mixed signal, i.e. analog-to-digital (ADC) and digital-to-analog converters (DAC). He is currently researching high-efficiency, scalable, and synthesizable ADCs.

B. Publications

S. Weaver et al., "Design Considerations for Stochastic Analog-to-Digital Conversion," *Electronics, Circuits, and Systems, 14th IEEE International Conference on*, pp. 234-237, December 2007.

S. Weaver et al., "A 6b Stochastic Flash Analog-to-Digital Converter Without Calibration or Reference Ladder," *Solid State Circuits Conference, IEEE Asian*, pp. 373-376, November 2008.

C. Academic Honors

OSU Presidential Scholar, awarded Fall 2002 - Spring 2006
Laurel Scholarship, awarded Fall 2006 - Spring 2007
AeA Intel Fellowship, awarded Fall 2006 - Spring 2008
AFRL Fellowship, awarded Winter 2008
ADI Outstanding Student Designer, awarded Winter 2009
SRC GRC Graduate Fellowship, announced Spring 2009

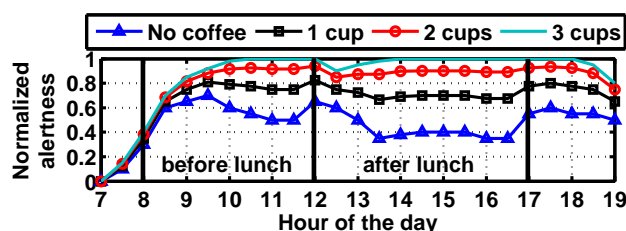


Fig. 1. Skyler Weaver's normalized alertness over a work day as affected by number of cups of coffee consumed. A decrease in alertness always appears following lunch; however, note that this effect is reduced by coffee.

TABLE I
TRIVIAL PERFORMANCE SUMMARY

Prefers vi or emacs	vi
Favorite Band	Journey
Favorite Sport	Marathoning
Favorite Starfleet Captain	Picard
Best Musical Instrument	Piano
Best Muppet Impersonation	Kermit the Frog

III. OBJECTIVE

Skyler would like to find a corporate connection to assist him in his continuing research. This would be an excellent opportunity for the company to see if Skyler would be a good fit for the company, and for Skyler to see if the company would be a good fit for him, as he will be seeking employment on completion of his research. Assistance could be provided as funding, a company contact/mentor, IC fabrication, paid internship, or all the above. Skyler awaits your phone call.

IV. CONCLUSION

Skyler Weaver is a highly qualified circuit designer with particular interest in mixed signal data converters. He enjoys using creative and innovative solutions to solve circuit problems. If teamed with a group of engineers with great experience in implementation and classical circuit techniques, there is no limit to what can be accomplished.

REFERENCES

- [1] Dr. Un-Ku Moon, advisor at Oregon State University, School of EECS, Oregon State University, 1148 Kelley Engineering Center, Corvallis, OR 97331., moon@eecs.oregonstate.edu.
- [2] Dr. Karti Mayaram, department head at Oregon State University, School of EECS, Oregon State University, 1148 Kelley Engineering Center, Corvallis, OR 97331., karti@eecs.oregonstate.edu.
- [3] Daniel Knierim, Tektronix Fellow, Tektronix, Inc., 14200 S.W. Karl Braun Drive, PO Box 500, Beaverton, OR 97077., daniel.g.knierim@xgate.tek.com.