

Skyler Weaver



Objective

- Explore and develop advanced, innovative solutions to modern IC design challenges through research and collaboration.
- Design analog/mixed-signal circuits for a forward-thinking company that produces cutting-edge ASICs.
- Bring fresh ideas and creativity to industry, while gaining considerable knowledge from experienced designers.

Education

Ph.D.

Doctor of Philosophy
Expected graduation: Fall 2010
Electrical Engineering, Oregon State University
Advisor: Dr. Un-Ku Moon

H.B.S.

Honors Bachelor of Science
Graduated: June 2006, Summa Cum Laude
Electrical Engineering, Oregon State University
GPA: 3.93/4.0 (rank = 1)

Publications

- **S. Weaver**, B. Hershberg, P. Kurahashi, D. Knierim, and U.-K. Moon, "Stochastic Flash Analog-to-Digital Conversion," Circuits and Systems I, IEEE Transactions on.
- B. P. Hershberg, **S. T. Weaver**, and U.-K. Moon, "A 1.4V Signal Swing Hybrid CLS-Opamp/ZCBC Pipelined ADC Using a 300mV Output Swing Opamp," Solid State Circuits Conference, IEEE International, February 2010.
- **S. Weaver**, B. Hershberg, P. K. Hanumolu, and U.-K. Moon, "A Multiplexer-Based Digital Passive Linear Counter (PLINCO)," Electronics, Circuits, and Systems, 16th IEEE International Conference on, December 2009.
- **S. Weaver**, B. Hershberg, D. Knierim, and U.-K. Moon, "A 6b Stochastic Flash Analog-to-Digital Converter Without Calibration or Reference Ladder," Solid State Circuits Conference, IEEE Asian, pp. 373-376, November 2008.
- **S. Weaver**, D. Knierim, R. Wazenried, and U.-K. Moon, "Design Considerations for Stochastic Analog-to-Digital Conversion," Electronics, Circuits, and Systems, 14th IEEE International Conference on, pp. 234-237, December 2007.

Awards

SRC GRC Graduate Fellowship

awarded Fall 2009 - present

2009 ICECS Best Paper

awarded December 2009

ADI Outstanding Student Designer

awarded Winter 2009

AFRL Fellowship

awarded Winter 2008

AeA Intel Fellowship

awarded Fall 2006 - Spring 2008

Laurel Scholarship

awarded Fall 2006 - Spring 2007

OSU Presidential Scholar

awarded Fall 2002 - Spring 2006

Skills

Hardware Design

- Analog/Mixed-signal IC design (Schematic, Layout, ESD, etc.)
- Board-level design, including microcontrollers

Computer Tools

- Cadence (Spectre, Virtuoso, Encounter)
- Mentor Graphics (Calibre)
- Synopsis (DC, Hercules)
- MATLAB/Simulink
- Mathematica
- SWITCAP

Lab Equipment Used

- Oscilloscope
- Logic analyzer
- Signal generators
- Spectrum analyzer
- Probe Station

Programming Languages

- C
- MATLAB
- SPICE
- AVR assembly
- VHDL
- SKILL (OCEAN scripts)
- LaTeX
- perl
- PHP, MySQL, HTML

Miscellaneous Skills

- Running marathons
- Playing piano
- Kermit the Frog impersonation

References

- Dr. Un-Ku Moon, advisor at Oregon State University, 1148 Kelley Engineering Center, Corvallis, OR 97331., moon@eecs.oregonstate.edu.
- Dr. Karti Mayaram, department head at Oregon State University, 1148 Kelley Engineering Center, Corvallis, OR 97331., karti@eecs.oregonstate.edu.
- Daniel Knierim, Tektronix Fellow, Tektronix, Inc., 14200 S.W. Karl Braun Drive, PO Box 500, Beaverton, OR 97077., daniel.g.knierim@exgate.tek.com.

Contact

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